

Digital Computers and Machine Representation of Data

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Bits and Bytes

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- ▶ 12v vs. 0v

Bits are organized into groups of eight: *bytes*

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Data Types

Computers are required to support various types of data.

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- ▶ Real and Complex Numbers
 - ▶ Complex numbers are just ordered pairs of real numbers.

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ASCII Code Chart

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	NUL	SOH	STX	ETX	EOT	ENQ	ACK	BEL	BS	HT	LF	VT	FF	CR	SO	SI
1	DLE	DC1	DC2	DC3	DC4	NAK	SYN	ETB	CAN	EM	SUB	ESC	FS	GS	RS	US
2		!	"	#	\$	%	&	'	()	*	+	,	-	.	/
3	0	1	2	3	4	5	6	7	8	9	:	;	<	=	>	?
4	@	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
5	P	Q	R	S	T	U	V	W	X	Y	Z	[\]	^	_
6	`	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
7	p	q	r	s	t	u	v	w	x	y	z	{		}	~	DEL

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- ▶ UTF-8 is designed so that ASCII is subsumed.

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- ▶ Memory addresses are one-word integers
- ▶ A 32-bit machine can have at most 4GB of memory

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In Matlab and Python 3.x we do not have to worry about this.

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- ▶ There are largest and smallest numbers that can be represented.

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Note that there is a sign bit for the mantissa, but the exponent is shifted.

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- ▶ i.e. $\sim 5e - 14$ to $\sim 1e19$
- ▶ For balance, we shift, instead of using sign bit.
- ▶ Avogadro's number (6.022×10^{23}) cannot be represented in 16-bit floating point.

IEEE 754

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 - ▶ 8-bit exponent, shifted range from -126 to +127

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 - ▶ 8-bit exponent, shifted range from -126 to +127
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 - ▶ 8-bit exponent, shifted range from -126 to +127
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- ▶ 64-bit floating point
 - ▶ 52-bit mantissa, 1 sign bit

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